

Daniel Maslowski





Agenda







Thanks for a start!



The development of dtvis has had strong support from our hackerspace.







Device Tree History



IEEE 1275¹ Standard for Boot (Initialization Configuration) Firmware: Core Requirements and Practices / Open Firmware²





- IEEE 1275¹ Standard for Boot (Initialization Configuration) Firmware: Core Requirements and Practices / Open Firmware²
- describing hardware topology for non-discoverable devices



- IEEE 1275¹ Standard for Boot (Initialization Configuration) Firmware: Core Requirements and Practices / Open Firmware²
- 🔋 describing hardware topology for non-discoverable devices
 - Linux, U-Boot, Zephyr, OLPC, FreeBSD and other projects use it
 including Apple back in the days



- IEEE 1275¹ Standard for Boot (Initialization Configuration) Firmware: Core Requirements and Practices / Open Firmware²
- 🔋 describing hardware topology for non-discoverable devices
 - Linux, U-Boot, Zephyr, OLPC, FreeBSD and other projects use it
 including Apple back in the days
- 🔋 not too elegant, attached to kernel via bindings



- IEEE 1275¹ Standard for Boot (Initialization Configuration) Firmware: Core Requirements and Practices / Open Firmware²
- 🔋 describing hardware topology for non-discoverable devices
- Linux, U-Boot, Zephyr, OLPC, FreeBSD and other projects use it
 including Apple back in the days
- not too elegant, attached to kernel via bindings
- only few specified fields, most are "as someone wrote them"
 compare e.g. Amlogic vs Allwinner SoC based trees



- IEEE 1275¹ Standard for Boot (Initialization Configuration) Firmware: Core Requirements and Practices / Open Firmware²
- 🔋 describing hardware topology for non-discoverable devices
- Linux, U-Boot, Zephyr, OLPC, FreeBSD and other projects use it
 including Apple back in the days
- 🔋 not too elegant, attached to kernel via bindings
- only few specified fields, most are "as someone wrote them"
 compare e.g. Amlogic vs Allwinner SoC based trees
- 🔋 can range from a few hundred to a thousand nodes





- IEEE 1275¹ Standard for Boot (Initialization Configuration) Firmware: Core Requirements and Practices / Open Firmware²
- 🔋 describing hardware topology for non-discoverable devices
- Linux, U-Boot, Zephyr, OLPC, FreeBSD and other projects use it
 including Apple back in the days
- 🔋 not too elegant, attached to kernel via bindings
- only few specified fields, most are "as someone wrote them"
 compare e.g. Amlogic vs Allwinner SoC based trees
- 🔋 can range from a few hundred to a thousand nodes
- 🔋 the tree is a lie; there are cycles, e.g., power supplies and clocks





There were discussions on tooling at Linux Plumbers³, partially stalled.

³https://elinux.org/images/8/83/Plumbers_2016_dt_device_tree_tools.pdf



There were discussions on tooling at Linux Plumbers³, partially stalled.

- Component Inspector (by Freescale, now NXP)
 - proprietary, closed source Eclipse plugin
 - was part of QorIQ Configuration Suite, no longer available

³https://elinux.org/images/8/83/Plumbers_2016_dt_device_tree_tools.pdf





There were discussions on tooling at Linux Plumbers³, partially stalled.

- Component Inspector (by Freescale, now NXP)
 proprietary, closed source Eclipse plugin
 was part of QorIQ Configuration Suite, no longer available
- https://github.com/dev-0x7C6/fdt-viewer
 - mixed tree + hex/text viewer, C++ + Qt
 - supports dtb, dtbo (overlay) and itb (FIT image)

³https://elinux.org/images/8/83/Plumbers_2016_dt_device_tree_tools.pdf⁴https://www.spinics.net/lists/devicetree-spec/msg00950.html



There were discussions on tooling at Linux Plumbers³, partially stalled.

- Component Inspector (by Freescale, now NXP)
 proprietary, closed source Eclipse plugin
 was part of QorIQ Configuration Suite, no longer available
- https://github.com/dev-0x7C6/fdt-viewer
 mixed tree + hex/text viewer, C++ + Qt
 supports dtb, dtbo (overlay) and itb (FIT image)
- https://github.com/bmx666/dtv-demo
 - "RFC DTV (Device Tree Visualiser)" on mailing list⁴
 - dt_s_ only, more of a text editor, Python + Qt6

³https://elinux.org/images/8/83/Plumbers_2016_dt_device_tree_tools.pdf
⁴https://www.spinics.net/lists/devicetree-spec/msg00950.html
⁵https://marketplace.visualstudio.com/items?itemName=plorefice.devicetree



There were discussions on tooling at Linux Plumbers³, partially stalled.

- Component Inspector (by Freescale, now NXP)
 proprietary, closed source Eclipse plugin
 was part of QorIQ Configuration Suite, no longer available
- https://github.com/dev-0x7C6/fdt-viewer
 mixed tree + hex/text viewer, C++ + Qt
 supports dtb, dtbo (overlay) and itb (FIT image)
- https://github.com/bmx666/dtv-demo
 - "RFC DTV (Device Tree Visualiser)" on mailing list⁴
 - dt_s_ only, more of a text editor, Python + Qt6
- \bigcirc VS Code plugin plorefice.devicetree 5
 - syntax highlighting + collapsing
 - could be enhanced with dtvis :-)

³https://elinux.org/images/8/83/Plumbers_2016_dt_device_tree_tools.pdf
 ⁴https://www.spinics.net/lists/devicetree-spec/msg00950.html
 ⁵https://marketplace.visualstudio.com/items?itemName=plorefice.devicetree







Design of dtvis





Recap: Platform System Interface



The *Platform System Interface* project (PSI) is a collection of design ideas, specifications, tools and other resources all around hardware platforms, firmware, bootloaders, OS interfacing and user experience.

https://github.com/platform-system-interface





Recap: Platform System Interface



The *Platform System Interface* project (PSI) is a collection of design ideas, specifications, tools and other resources all around hardware platforms, firmware, bootloaders, OS interfacing and user experience.

https://github.com/platform-system-interface

Talk: Platform System Interface - Design and Evaluation of Computing as a Whole

in-depth discussion of design paradigms and complexity in computing https://metaspora.org/platform-system-interface-computing-as-whole.pdf



What is dtvis?

⁶https://devicetree.org



What is dtvis?

dtvis is a DeviceTree⁶ visualizer.

⁶https://devicetree.org



What is dtvis?

dtvis is a DeviceTree⁶ visualizer.



https://github.com/platform-system-interface/dtvis

⁶https://devicetree.org



Challenges when Drawing Trees

- How do we distribute nodes without overlaps?
 - How do we make "good" use of space?
- Nodes can have few to dozens of properties.





Challenges when Drawing Trees

- How do we distribute nodes without overlaps?
- How do we make "good" use of space?
- Nodes can have few to dozens of properties.







Challenges when Drawing Trees

How do we distribute nodes without overlaps?

- How do we make "good" use of space?
- Nodes can have few to dozens of properties.



Strategies



Even distribution



dtvis Design

Node distribution in tree visualization



Currently, we have no overlaps, but trees may grow very wide.

















.

What if...

Ψ



... we compile Rust...





... we compile Rust...

... to Wasm...









... and use it in an app?

... to Wasm...

... we compile Rust...











... we compile Rust... ... to Wasm... ... and use it in an app?

Magic happens - we can use native code on web platforms!









Getting started

https://lannonbr.com/blog/2020-01-07-rust-wasmpack/ https://rustwasm.github.io/docs/wasm-pack/





Getting started

https://lannonbr.com/blog/2020-01-07-rust-wasmpack/ https://rustwasm.github.io/docs/wasm-pack/

TL;DR

cargo install wasm-pack wasm-pack new my-rust-wasm-foo





Getting started

https://lannonbr.com/blog/2020-01-07-rust-wasmpack/ https://rustwasm.github.io/docs/wasm-pack/

TL;DR

cargo install wasm-pack wasm-pack new my-rust-wasm-foo

The glue

https://github.com/wasm-tool/wasm-pack-plugin https://rustwasm.github.io/docs/wasm-pack/tutorials/hybrid-applicationswith-webpack/using-your-library.html



Getting started

https://lannonbr.com/blog/2020-01-07-rust-wasmpack/ https://rustwasm.github.io/docs/wasm-pack/

TL;DR

cargo install wasm-pack wasm-pack new my-rust-wasm-foo

The glue

https://github.com/wasm-tool/wasm-pack-plugin https://rustwasm.github.io/docs/wasm-pack/tutorials/hybrid-applicationswith-webpack/using-your-library.html

More glue

cargo add gloo-utils



The Rust side

Y



The Rust side

extern crate wasm_bindgen; use gloo_utils::format::JsValueSerdeExt; use serde::{Deserialize, Serialize}; use wasm_bindgen::prelude::*;

/// ...

```
#[derive(Serialize, Deserialize)]
struct Foo {
    bar: u32,
    baz: String,
```

```
#[wasm_bindgen]
pub fn some_fun(data: JsValue) -> JsValue {
    /// ...
    let foo = Foo::new { bar: 42, baz: "Rust Wasm" };
    JsValue::from_serde(&foo).unwrap()
```





The JavaScript side

import { some_fun } from "./rs/pkg";

const res = some_fun({ woopWoop: 1337 });

console.info(res);





The JavaScript side

import { some_fun } from "./rs/pkg";

const res = some_fun({ woopWoop: 1337 }); console.info(res);

/* ... */

But that is synchronous and blocking!





The JavaScript side

import { some_fun } from "./rs/pkg";

const res = some_fun({ woopWoop: 1337 }); console.info(res); ... */

But that is synchronous and blocking!

https://rustwasm.github.io/wasm-bindgen/reference/js-promises-and-rust-futures.html

https://rustwasm.github.io/wasm-bindgen/api/wasm_bindgen_futures/











Follow Me



Daniel Maslowski

https://github.com/orangecms https://twitter.com/orangecms https://mastodon.social/@cyrevolt https://twitch.tv/cyrevolt https://youtube.com/@cyrevolt

https://github.com/platform-system-interface https://metaspora.org/visualizing-device-trees.pdf License: CC BY 4.0 https://creativecommons.org/licenses/by/4.0/

